

# **Gate Turn-Off Thyristor**

DS4334-5 July 2014 (LN31737)

#### **FEATURES**

- Double Side Cooling
- High Reliability In Service
- High Voltage Capability
- Fault Protection Without Fuses
- High Surge Current Capability
- Turn-off Capability Allows Reduction In Equipment Size And Weight. Low Noise Emission Reduces Acoustic Cladding Necessary For Environmental Requirements

### **APPLICATIONS**

- Variable speed A.C. motor drive inverters (VSD-AC).
- Uninterruptable Power Supplies
- High Voltage Converters.
- Choppers.
- Welding.
- Induction Heating.
- DC/DC Converters.

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 $V_{\text{DRM}}$  4500V  $I_{\text{T(AV)}}$  1100A  $dV_{\text{D}}/dt$  750V/ $\mu$ s  $dI_{\text{T}}/dt$  300A/ $\mu$ s

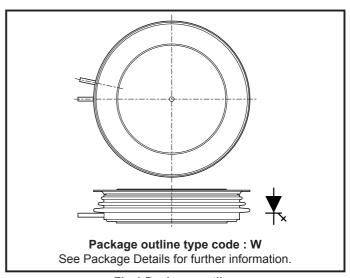


Fig.1 Package outline

#### **VOLTAGE RATINGS**

Type Number	Repetitive Peak Off-state Voltage V <sub>DRM</sub> V	Repetitive Peak Reverse Voltage V <sub>RRM</sub> V	Conditions
DG858DW45	4500	16	$T_{vj} = 125^{\circ}C, I_{DRM} = 100 \text{mA},$ $I_{RRM} = 50 \text{mA}$

#### **CURRENT RATINGS**

Symbol	Parameter	Conditions	Max.	Units
I <sub>TCM</sub>	Repetitive peak controllable on-state current	$V_{\rm D} = V_{\rm DRM}, T_{\rm j} = 125 {\rm ^{\circ}C},  {\rm di}_{\rm GQ}/{\rm dt} = 40 {\rm A}/{\rm \mu s},  {\rm Cs} = 4.0 {\rm \mu F},  {\rm L}_{\rm S} \leq 200 {\rm nH}$	3000	Α
I <sub>T(AV)</sub>	Mean on-state current	$T_{HS} = 80^{\circ}C$ . Double side cooled, half sine 50Hz.	1100	Α
I <sub>T(RMS)</sub>	RMS on-state current	T <sub>HS</sub> = 80°C. Double side cooled, half sine 50Hz.	1720	Α

# **SURGE RATINGS**

Symbol	Parameter	Conditions	Max.	Units
I <sub>TSM</sub>	Surge (non-repetitive) on-state current	10ms half sine. T <sub>j</sub> = 125°C	20.0	kA
l²t	I <sup>2</sup> t for fusing	10ms half sine. T <sub>j</sub> =125°C	2.0 x 10 <sup>6</sup>	A²s
di <sub>T</sub> /dt	Critical rate of rise of on-state current	$V_D = 3000V, I_T = 3000A, T_j = 125^{\circ}C$ $I_{FG} > 40A, Rise time < 1.0 \mu s$	300	A/μs
dV <sub>D</sub> /dt	Rate of rise of off-state voltage	To 66% $V_{DRM}$ ; $R_{GK} \le 22\Omega$ , $T_j = 125$ °C	20	V/μs
		To 66% V <sub>DRM</sub> ; V <sub>RG</sub> = -2V, T <sub>j</sub> = 125°C	750	V/μs
L <sub>s</sub>	Peak stray inductance in snubber circuit	$I_T = 3000A$ , $V_D = V_{DRM}$ , $T_j = 125$ °C, $di_{GQ}/dt = 40A/\mu s$ , $Cs = 4.0\mu F$	200	nH

# **GATE RATINGS**

Symbol	Parameter	Conditions	Min.	Max.	Units
$V_{RGM}$	Peak reverse gate voltage	This value maybe exceeded during turn-off	-	16	V
I <sub>FGM</sub>	Peak forward gate current		20	100	А
P <sub>FG(AV)</sub>	Average forward gate power		-	20	W
P <sub>RGM</sub>	Peak reverse gate power		-	24	kW
di <sub>GQ</sub> /dt	Rate of rise of reverse gate current		20	60	A/μs
t <sub>ON(min)</sub>	Minimum permissable on time		50	-	μs
t <sub>OFF(min)</sub>	Minimum permissable off time		100	-	μs

## THERMAL AND MECHANICAL DATA

Symbol	Parameter	Conditions		Min.	Max.	Units
		Double side cooled		-	0.011	°C/W
$R_{th(j-hs)}$	R <sub>th(j-hs)</sub> DC thermal resistance - junction to heatsink surface	Anode side cooled		-	0.017	°C/W
		Cathode side cooled		-	0.03	°C/W
R <sub>th(c-hs)</sub>	Contact thermal resistance	Clamping force 40kN With mounting compound	per contact	-	0.0021	°C/W
T <sub>vj</sub>	Virtual junction temperature		•	-40	125	°C
T <sub>OP</sub> /T <sub>stg</sub>	Operating junction/storage temperature range			-40	125	°C
-	Clamping force			36.0	44.0	kN

## **CHARACTERISTICS**

Symbol	Parameter	Conditions	Min.	Max.	Units
$V_{TM}$	On-state voltage	At 3000A peak, I <sub>G(ON)</sub> = 10A d.c.	-	3.85	V
I <sub>DM</sub>	Peak off-state current	V <sub>DRM</sub> = 4500V, V <sub>RG</sub> = 2V	-	100	mA
I <sub>RRM</sub>	Peak reverse current	At V <sub>RRM</sub>	-	50	mA
$V_{\rm GT}$	Gate trigger voltage	V <sub>D</sub> = 24V, I <sub>T</sub> = 100A, T <sub>j</sub> = 25°C	-	1.2	V
I <sub>GT</sub>	Gate trigger current	$V_{D} = 24V, I_{T} = 100A, T_{j} = 25^{\circ}C$	-	4.0	А
I <sub>RGM</sub>	Reverse gate cathode current	V <sub>RGM</sub> = 16V, No gate/cathode resistor	-	50	mA
E <sub>on</sub>	Turn-on energy	V <sub>D</sub> = 2000V	-	4400	mJ
t <sub>d</sub>	Delay time	$I_{T} = 3000A, dI_{T}/dt = 300A/\mu s$	-	2.0	μs
t <sub>r</sub>	Rise time	I <sub>FG</sub> = 40A, rise time < 1.0μs	-	6.0	μs
E <sub>OFF</sub>	Turn-off energy		-	12500	mJ
t <sub>gs</sub>	Storage time		-	26	μs
t <sub>gf</sub>	Fall time	I <sub>T</sub> = 3000A, V <sub>DM</sub> = 4200V	-	2.5	μs
t <sub>gq</sub>	Gate controlled turn-off time	Snubber Cap Cs = 4.0μF,	-	28.5	μs
$Q_{GQ}$	Turn-off gate charge	$di_{GQ}/dt = 40/\mu s$	-	12500	μС
$Q_{GQT}$	Total turn-off gate charge		-	25000	μС
I <sub>GQM</sub>	Peak reverse gate current		_	950	А

## **RELIABILITY**

	Conditions	Limit	Units
DC blocking reliability	$V_{dc}$ = 3500V, $T_{j}$ = -40 to + 125°C, ambient cosmic radiation at sea level, in open air, 100% duty cycle.	100	FIT

# **CURVES**

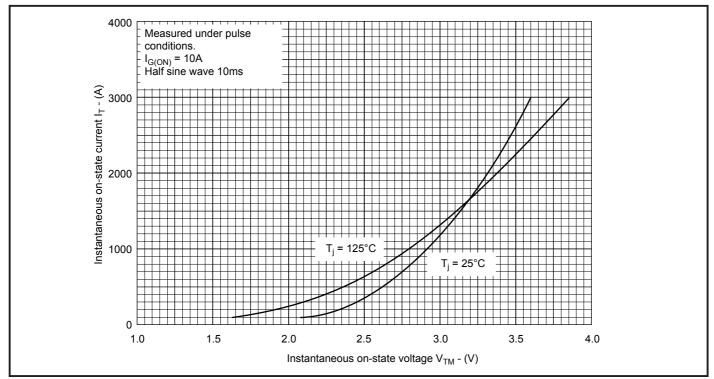


Figure 2. On-state characteristics

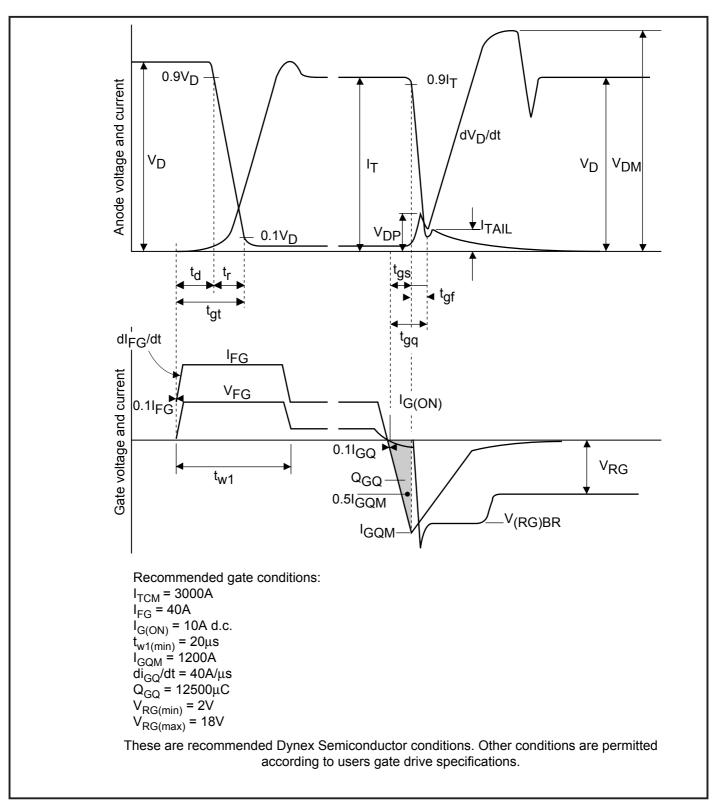
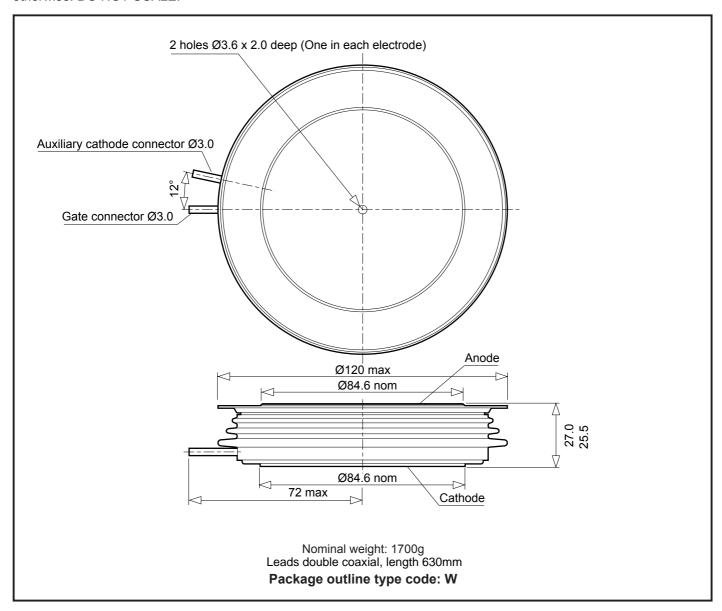


Figure 3. General switching waveforms

### **PACKAGE DETAILS**

For further package information, please contact your local Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



## **Associated Literature/Products**

Publication No. Title/Part Number

AN45/1	Application note - GDU9X-XXXXX Series GTO gate drive units.
DS4567	GDU90-20721 GTO gate drive unit.
DS4568	GDU90-20722 GTO gate drive unit.
DS4150	DSF8045SK - Snubber diode.
DS4153	DSF21545SV - Antiparallel/freewheel diode.



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